

REMARKS

Claims 1-10 have been examined claims 2-4, 6-8, and 10 are amended herein. Accordingly, claims 1-10 are now pending in the application. It is noted with appreciation that claims 1-3 and 7-10 are indicated as reciting allowable subject matter. Reexamination and reconsideration of all objections and rejections is requested.

Claims 2-3 and 7-10 have been amended in response to the claim objections.

Claim 6 has been amended to recite additional features. It is believed that amended claim 6 is patentable over the cited prior art. Claims 4-10 which depend on claim 4 are thus also allowable.

Annotated replacement Figs. 1 and 2 are included. Annotated Fig. 2 addresses the issues raised in paragraph 3 office action and annotated Fig. 1 addresses the issues raised in paragraph 5 of the office action.

New Fig. 1A has been submitted to address the issues raised in paragraph 4 of the office action. No new matter is introduced because the figure conforms to the specification.


The specification has been amended to refer to new Fig. 1A.

CONCLUSION

In view of the foregoing, Applicants believe all claims now pending in this Application are in condition for allowance. The issuance of a formal Notice of Allowance at an early date is respectfully requested.

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at (925) 944-3320.

Respectfully submitted,


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SERVER

CLIENT

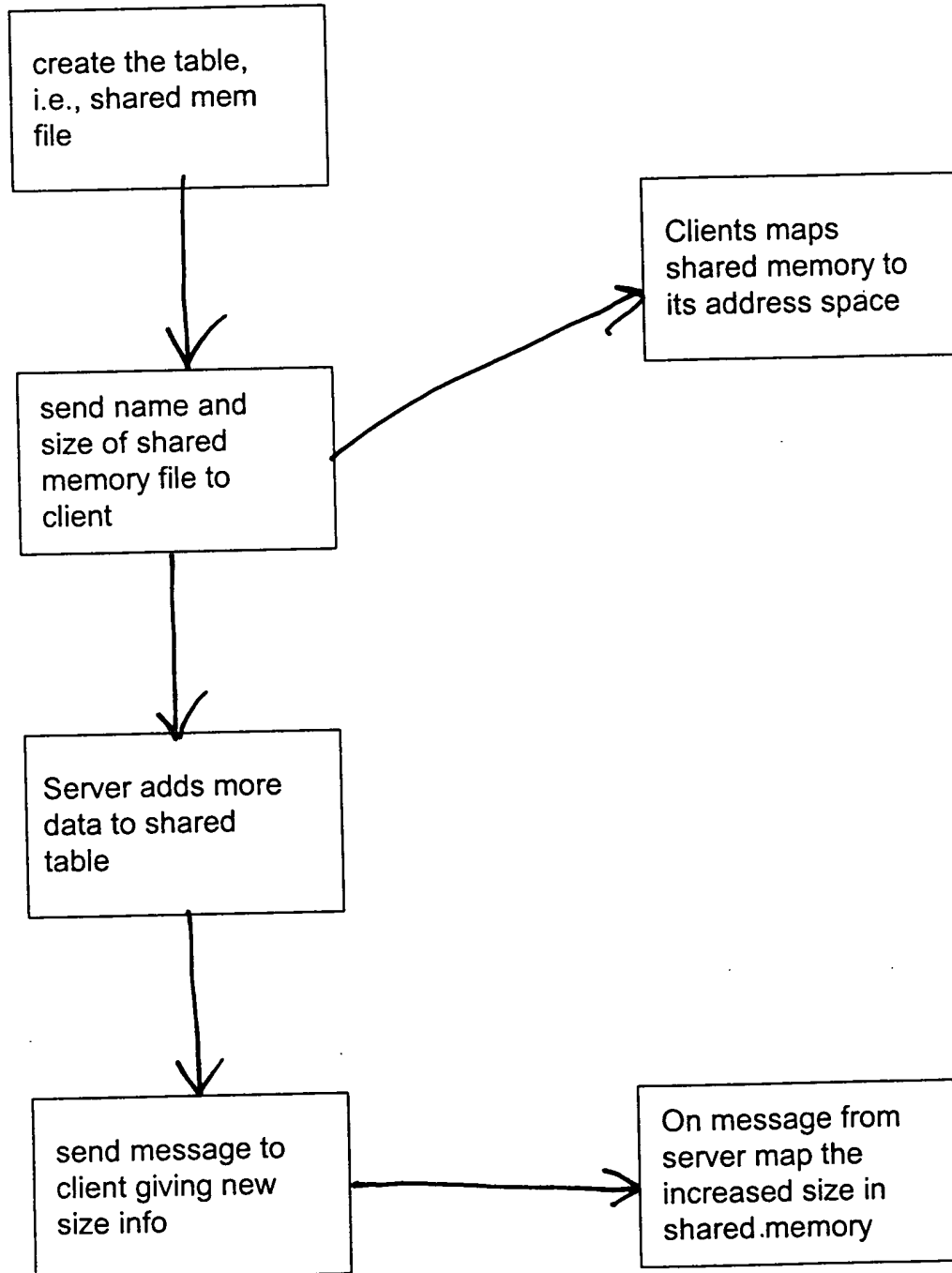


Fig. 1

PRIOR ART



~~SERVER~~
SYSTEM
CODE

~~CLIENT~~
FIRST
PROCESS

~~server adds more
data to shared
table~~

ALLOCATE
ADDITIONAL
SHARED MEMORY

MAINTAIN ACCESS
INFORMATION IDENTIFYING
ALL CLIENT PROCESSES
HAVING ACCESS TO SHARED MEMORY

~~server accesses
PIDs of all clients
attached to shared
memory~~

IDENTIFY A SECOND
PROCESS HAVING
ACCESS TO SHARED
MEMORY

MAP ADDITIONAL
SHARED MEMORY
INTO ADDRESS SPACE
OF SECOND PROCESS

~~server uses
mmap_peer () to
map added shared
memory into address
space of all clients~~

USE PEER MAPPING
SYSTEM CALL TO
REQUEST MAPPING
ADDITIONAL SHARED
MEMORY INTO
ADDRESS SPACE
OF SECOND
PROCESS

Fig. 2